

[Poster Session I] August 31 (Mon.), 16:00-17:40 / Grand Ballroom B

No.	Submission ID	Author	Title
1-1	1024	Federico Dalmagioni (University of New South Wales)	Operating a Foundry-Fabricated Linear Array of SiMOS Spin Qubits
1-2	1047	Brianna J Casey (University of Technology Sydney,)	Spin control in a multiplexed SiMOS characterisation device
1-3	1071	Conor Power (Equal1)	Semi-Classical Many-Body Admittance Extraction for Charge Sensing
1-5	1087	Arne Loenders (KU Leuven, IMEC)	Understanding oxide-thickness-dependent variability in dense Si-MOS quantum-dot arrays
1-6	1124	Isaac Vorreiter (University of New South Wales)	Charge sensing a p-type triple quantum dot fabricated on a 300 mm CMOS process
1-7	1126	Angus A Russell (University of Cambridge, Quantum Motion)	Mass Characterization of Quantum Dots in a 300 mm Si MOS Crossbar Array
1-8	1140	Rick Silver (NIST)	Fabrication and Measurement of Atom-scale Quantum Dot Arrays for Analog Quantum Simulation
1-9	1049	Marion Bassi (TU Delft / QuTech)	Shared-control shuttling link between distant germanium spin qubit registers
1-10	1101	Pietro Borghi (TU Delft)	Shuttling and exchange coupling in a bilayer germanium quantum dot array
1-11	1169	Yun Ma (University of Science and Technology of China)	Simulation of Electron Shuttling Dynamics in Silicon Quantum-Dot Spin Qubits
1-12	1017	Patricia Oehrl (Walther-Meissner-Institute, Technical University Munich)	Squeezed microwave-spin coupling in phosphorus doped silicon: Quantum state storage and spin squeezing
1-13	1033	Chi-Wei Lee (National Tsing Hua University)	Measurement and Simulation of Ge Hole Spin Qubits Toward Circuit- QED Integration
1-14	1104	Sofia M Patomäki (Technische Universiteit Delft)	Photonic interface for gate-defined quantum dot spins
1-15	1116	Xiao-Fei Liu (Beijing Academy of Quantum Information Sciences)	Remote interaction between quantum dots under Floquet driving
1-16	1129	Elena Acinapura (EPFL)	Circuit QED architecture on planar Ge for spin-photon coupling
1-17	1006	Tatsuya Matsuda (The university of Tokyo)	TLF amplitude dependence of noise suppression via correlation based feedback in silicon quantum dots
1-18	1039	gregoire rousely (CEA-LETI)	High-Throughput Charge Noise Characterization Framework for CMOS-based Spin-Qubit Devices

[Poster Session I] August 31 (Mon.), 16:00-17:40 / Grand Ballroom B

No.	Submission ID	Author	Title
1-19	1053	Itamar Oren (University of New South Wales)	A Novel Approach to Qubit Frequency Tracking: Theory and Design of the Adiabatic Tangentially-Modulated (ATM) Pulse
1-20	1079	Michael Wolfe (University of Maryland College Park)	Si/SiGe spin qubit readout at elevated temperatures
1-21	1096	Hyoju Hwang (Seoul National University)	Si/SiGe spin qubit as an in situ probe of magnetic-field noise
1-22	1011	Patrick Del Vecchio (QuTech, TU Delft)	Tailoring spin-orbit interactions in Ge hole spin qubits
1-23	1040	Marvin Hartwig Zoellner (IHP-Leibniz Institute for High Performance)	Strain gradient for electron spin-qubits: splitting of Δ_z valleys in Si quantum wells
1-24	1041	Dennis van der Bovenkamp (University of Twente)	Effect of gate induced strain on the silicon valence band structure
1-25	1045	Bilal Tariq (National Center for Physics)	Two-electron spectrum of a silicon quantum dot
1-26	1060	Mingue Kim (JeonBuk University)	Quantum Point Contact Spectroscopy of Electrically Tunable Spin-Resolved Subbands in Multilayer WSe ₂
1-27	1076	Adrian Culver (University of California, Los Angeles)	Self-consistent device simulation of germanium hole qubits
1-28	1081	Guillermo Haas (CNRS)	A resonantly driven spin-valley Qubit with Dispersive Readout in Silicon
1-29	1090	LUCKY DONALD LYNGDOH KYNSHI (IISER Thiruvananthapuram)	Confinement-enhanced valley splitting in undoped Si/SiGe quantum well devices
1-30	1008	Jieyin Zhang (Dongguan Institute of Materials Science and Technology)	High Mobility Si/SiGe and Ge/SiGe Heterostructures Grown by Molecular Beam Epitaxy
1-31	1021	Marvin Hartwig Zöllner (IHP GmbH)	Thermal Budget Tolerance of Si/SiGe Quantum Wells
1-32	1091	Jian-Huan Wang (Beijing Academy of Quantum Information Sciences)	Controlled Growth of High Mobility Ge Nanowires for Qubit Scaling
1-33	1122	Ho yeon Yoo (Korea Advanced Institute of Science and Technology)	Electrostatic control of multi-quantum-dot systems in bilayer graphene
1-34	1018	takuma kuno (Hitachi, Ltd.)	Double-dressed qubit for robust and high-precision control
1-35	1028	Haotian Jiang (USTC)	In-plane Anisotropy of Qubit Properties in Planar Germanium

[Poster Session I] August 31 (Mon.), 16:00-17:40 / Grand Ballroom B

No.	Submission ID	Author	Title
1-36	1121	Helen V Sawyer (University College London, Quantum Motion)	Long spin dephasing times in a four-qubit processor made using SiMOS quantum dots
1-37	1127	Vatsal K Bandaru (University of Pennsylvania)	Probing limitations to fidelity in multielectron Si-MOS spin qubits
1-38	1155	jin yi (18226131422)	Achieve high-fidelity quantum operations through GRAPE algorithm
1-39	1009	Aldo Tarascio (University of Basel)	Self-Impedance matched nonreciprocal devices using gated edge magnetoplasmons
1-40	1012	Lea Schreckenber (Peter Gruenberg Institute - Integrated Computing Architectures (ICA / PGI-4),	Cryogenic 22nm FD-SOI CMOS Pulse Generation for Spin Qubit Shuttling
1-41	1022	SHOU-CHUNG CHEN (National Tsing Hua University)	Simplifying Cryogenic Routing: Design and Simulation of High-Frequency FPC CPW and Sample PCB for Spin Qubits
1-42	1032	Adam Quinn (Fermi National Accelerator Laboratory)	Design and Modeling of the Charge Readout of a SiMOS Quantum Dot with a Single Electron Transistor and cryoCMOS
1-43	1037	Mizuki Aitani (Kobe University)	Fast Auto-Tuning of Quantum Dots Using a Two-Stage Optimization Framework with Generative Model-Based Data Augmentation
1-44	1042	Fabian Hader (JARA-FIT Institute for Quantum Information, Forschungszentrum Jülich GmbH and RWTH Aachen	High-throughput cryogenic characterization of industrial Si/SiGe spin qubit devices
1-45	1054	Alice Petrillo (TuDelft, QuTech)	Towards Autonomous Qubit Position Identification and Shuttling via Gate-Controlled Spin Evolution
1-46	1077	Ron Nodel (UCLA)	Quantitative Simulation of Charge Stability Diagrams in Si/SiGe Devices
1-47	1102	Paul Surrey (JARA-FIT Institute for Quantum Information, Forschungszentrum Jülich GmbH and RWTH Aachen	Automated Wafer-Scale Dot Formation and Stability Characterization of Quantum Dot Devices
1-48	1031	Gauri Goenka (UNSW)	Germanium Nuclear Spin Coupled to SiMOS Quantum Dots
1-49	1036	Quim Torrent Nicolau (University of Twente)	Advances and challenges of single ²⁰⁹ Bi donors in Si nanoelectronic devices
1-50	1052	Ivana Bosnjak (University of Twente)	Towards Gallium acceptor qubits in silicon
1-52	1108	Maicol Ochoa (University of Maryland College Park and National Institute of Standards and Technology)	Local spin structure in phosphorus arrays in Silicon
1-53	1029	JoYu Wang (National Tsing Hua University)	Computer-Aided Design of Ge/SiGe Quadruple Quantum Dots for Hole Spin Qubits

[Poster Session I] August 31 (Mon.), 16:00-17:40 / Grand Ballroom B

No.	Submission ID	Author	Title
1-54	1043	Yun-Pil Shim (The University of Texas at El Paso)	Minimal Singlet-Singlet Qubit
1-55	1044	Tetsufumi Tanamoto (Teikyo University)	Device/circuit simulations of spin qubits using a gate-all-around transistor
1-56	1085	Damien Crielgaard (QuTech)	Exchange-only qubits in a hole-based semiconductor platform
1-57	1003	Kelvin Onggadinata (Nanyang Technological University)	Universal phase-error-transparent gate set for high-spin cat codes
1-58	1013	Christian Ventura Meinersen (QuTech, TU Delft)	Many-body crosstalk reduction with g-tensor engineering
1-59	1026	Yusuke Kanno (Hitachi Ltd.)	Analysis on the Local Structure of Electrostatic Potentials in Multilayer Dielectric Systems
1-60	1030	Nard Dumoulin Stuyck (Dirac)	CMOS compatibility of semiconductor spin qubits
1-61	1035	Gorka Aizpurua (Quantum Motion)	Phase Transition Single Electron Transistor
1-62	1066	Nikki Ebadollahi (University of Maryland)	Silicon Color Centers as a Prospect for Quantum Transduction
1-63	1072	Austin J Schleusner (Michigan State University)	Towards electron on helium spins and superconducting qubit hybrid systems
1-64	1195	Yu He (International Quantum Academy and Shenzhen Branch, Hefei National Laboratory, China)	Universal logical operations in a silicon quantum processor